

Study Committee on Global Microelectronics: Models for the Department of Defense in Semiconductor Public-Private Partnerships



Keck Center, Room 106 500 Fifth Street NW, Washington, DC 20001

Study Committee on Global Microelectronics: Models for the Department of Defense in Semiconductor Public-Private Partnerships under the auspices of the National Materials and Manufacturing Board (NMMB)

Meeting Agenda

July 25, 2023			
10:30 am ET/ 7:30 am PT	Open Session: Historical views, including lessons learned from Sematech	60 mins	
	 Dan Hutcheson, VLSI Research Q&A 		
11:30 am ET/ 8:30 am PT	Break	15 mins	

11:45 am ET/ 8:45 am PT	Open Session: Innovation Policy - Sujai Shivakumar, Director and Senior Fellow, Renewing American Innovation Project, Center for Strategic and International Studies - Q&A	60 mins
12:30 pm ET/ 9:30 am PT	Closed Session and Working Lunch •	60 mins
1:30 pm ET/ 10:30 am PT	Open Session: Outlooks and Analysis of the Semiconductor Ecosystem Boston Consulting Group Ramiro Palma, Managing Director and Partner (Austin) Thomas Lopez, Principal (Dallas) Deloitte Mark LaViolette, Managing Director, supply Chains & Network Operations, Government and Public Services Duncan Stewart, Director of Research for the Technology, Media, and Telecommunications Industry for Deloitte Canada Christie Simons, US Audit and Assurance Technology, Media, and Telecommunications Industry Leader Q&A	60 mins
2:30 pm ET/ 11:30 am PT	Open Session: Industry Views Tyler Schmidt, Intel Federal LLC Q&A	60 mins
3:30 pm ET/ 12:30 pm PT	Open Session Adjourns	

July 26, 2023			
9:30 am ET/ 6:30 am PT	Open Session: Department of Defense views, including Microelectronics Commons • Victoria Coleman, United States Air Force Chief Scientist • Q&A	60 mins	
10:30 am ET/ 7:30 am PT	Closed Discussion	60 mins	
11:30 am ET/ 8:30 am PT	 Open Session: Department of Defense views, including Microelectronics Commons Antonio de la Serna, Senior Director, Microelectronics	60 mins	
12:30 pm ET/ 9:30 am PT	Open Session Adjourns		